

-3-

In the Specification:

Please replace the paragraph in the specification on page 3 at lines 12-20 with the text below:

In Figure 2, a series of flip-flops 30, 32, ..., 34 are shown to be connected to one another in a way such that the output of each preceding flip-flop 30 is connected to the input of a succeeding flip-flop 32. The contents of the flip-flops are shifted in serially through a data-in terminal 38 of a two-input multiplexer 36. The output of each flip-flop connects to an individual switch, which can come in the form of a MOS transistor  $F_1, F_2, \dots, F_n$ , like those shown in Figure 1. Each transistor switch is connected to resistors  $R_1, R_2, \dots, R_n$  coupled in parallel to a voltage output.

In the paragraph that starts on page 3 at lines 32-35 and continues on page 4 on lines 1-35, please insert the indicated text (underlined) on page 4, at line 3:

When a chip is powering up, and before the flip-flops are loaded, the switches  $F_1, F_2, \dots, F_n$  need to be set to a default condition for the chip to operate correctly. Figure 3 shows a preferred embodiment of the present invention wherein additional circuitry is employed to provide a set of default values for the output of the flip-flops 30, 32, ..., 34. Figure 3 includes a multiplexer 36, a series of flip-flops 30-34, switches  $F_1, F_2, \dots, F_n$  and resistors  $R_1, R_2, \dots, R_n$  like those shown in Figure 2. In Figure 3, the output of each flip-flop connects to the drain of a PMOS transistor...

The resistors are described by original claim 10.  
No new matter has been added.

Applicants respectfully request that the above amendments to the specification be entered.